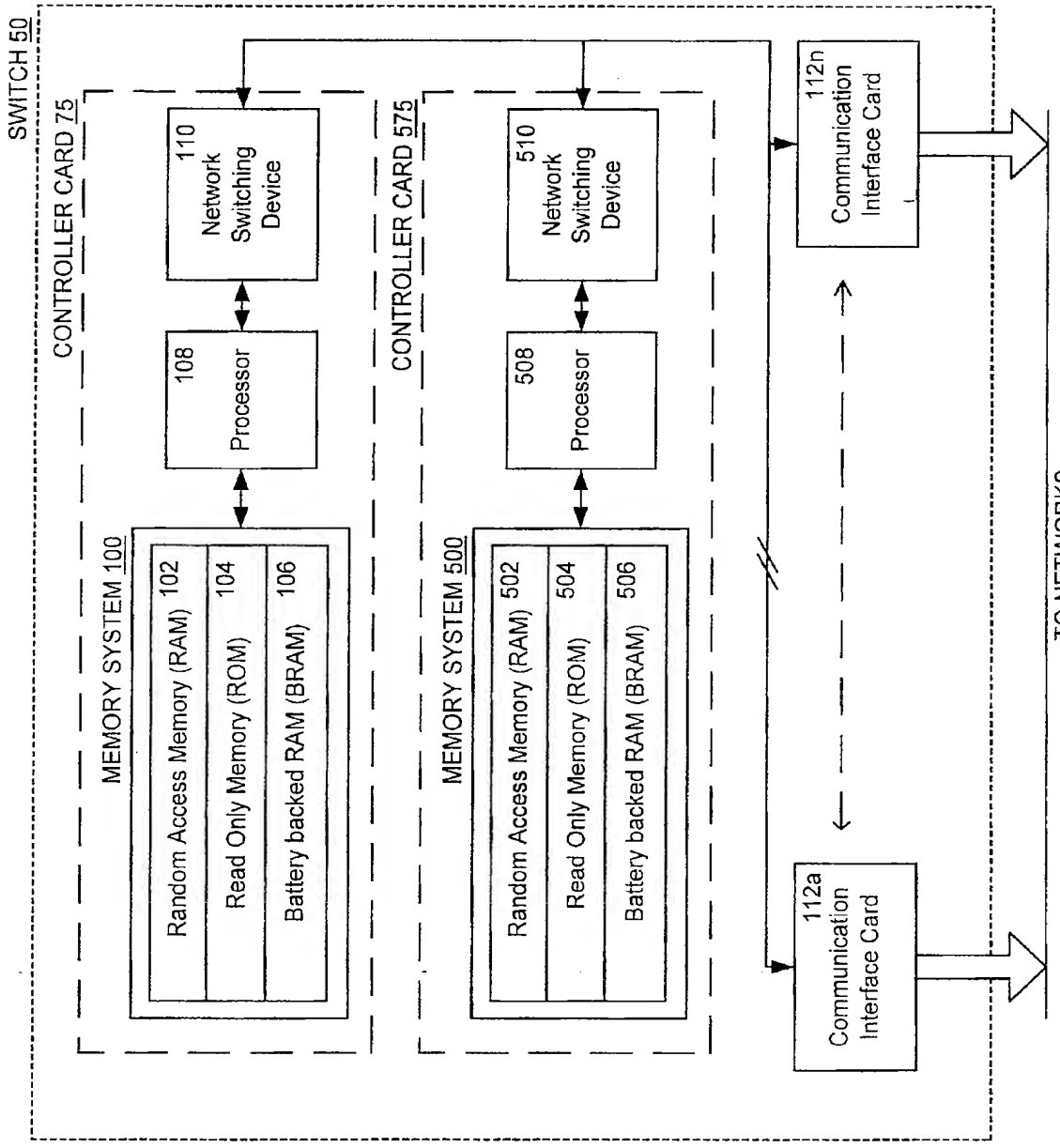
SWITCH 50
CONTROLLER CARD 75
MEMORY SYSTEM 100
Random Access Memory (RAM) 102
Read Only Memory (ROM) 104
Battery backed RAM (BRAM) 106
108 Processor
110 Network Switching Device
CONTROLLER CARD 575
MEMORY SYSTEM 500
Random Access Memory (RAM) 502
Read Only Memory (ROM) 504
Battery backed RAM (BRAM) 506
508 Processor
510 Network Switching Device
112a Communication Interface Card
112n Communication Interface Card
TO NETWORKS

Fig. 1

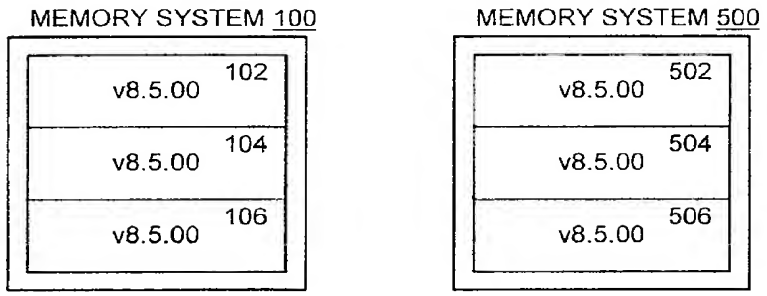

Fig.2a

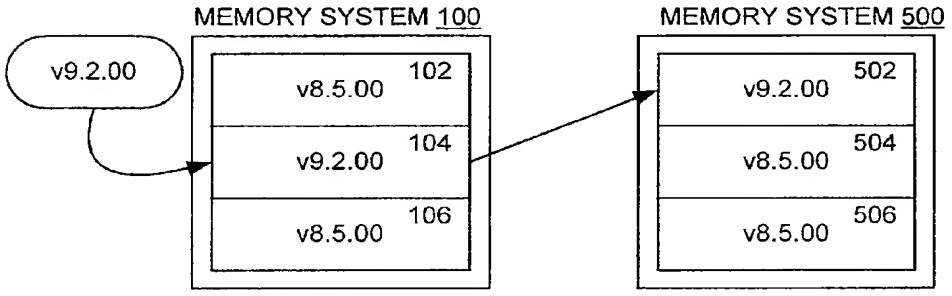

Fig.2b

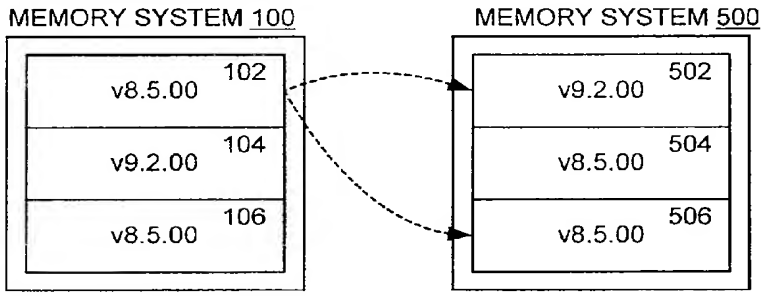

Fig.2c

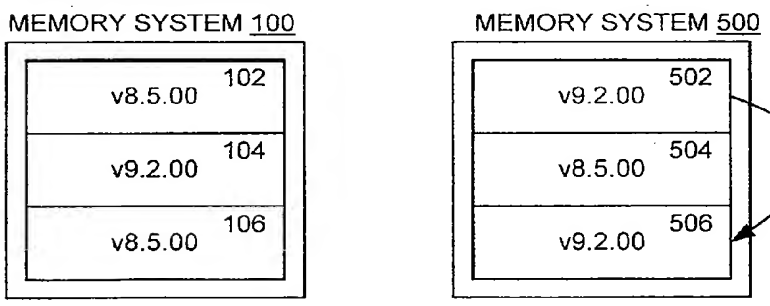

Fig.2d

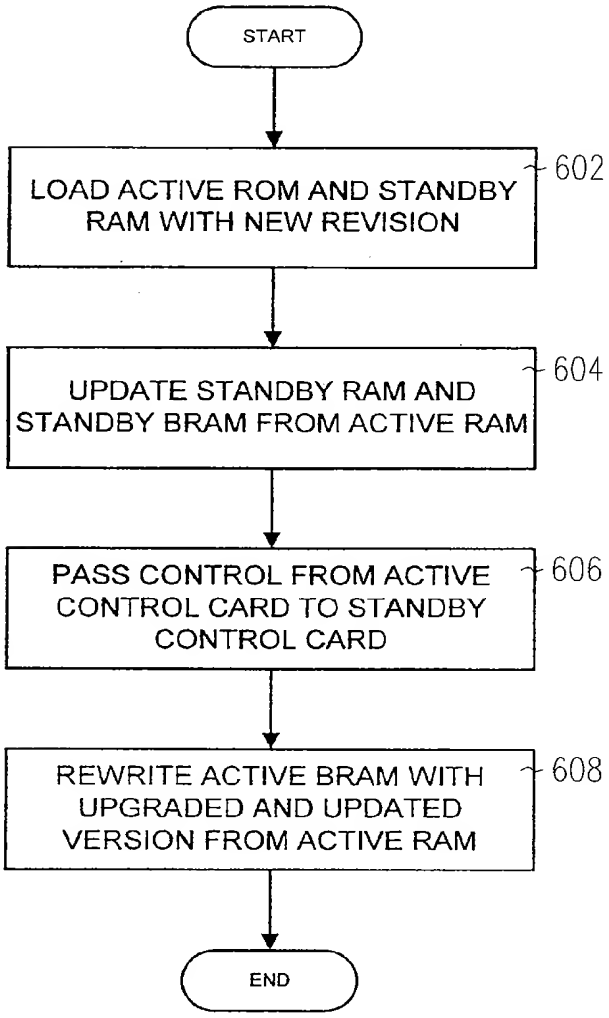
START
LOAD ACTIVE ROM AND STANDBY RAM WITH NEW REVISION
602
UPDATE STANDBY RAM AND STANDBY BRAM FROM ACTIVE RAM
604
PASS CONTROL FROM ACTIVE CONTROL CARD TO STANDBY CONTROL CARD
606
REWRITE ACTIVE BRAM WITH UPGRADED AND UPDATED VERSION FROM ACTIVE RAM
608
END

Fig. 3

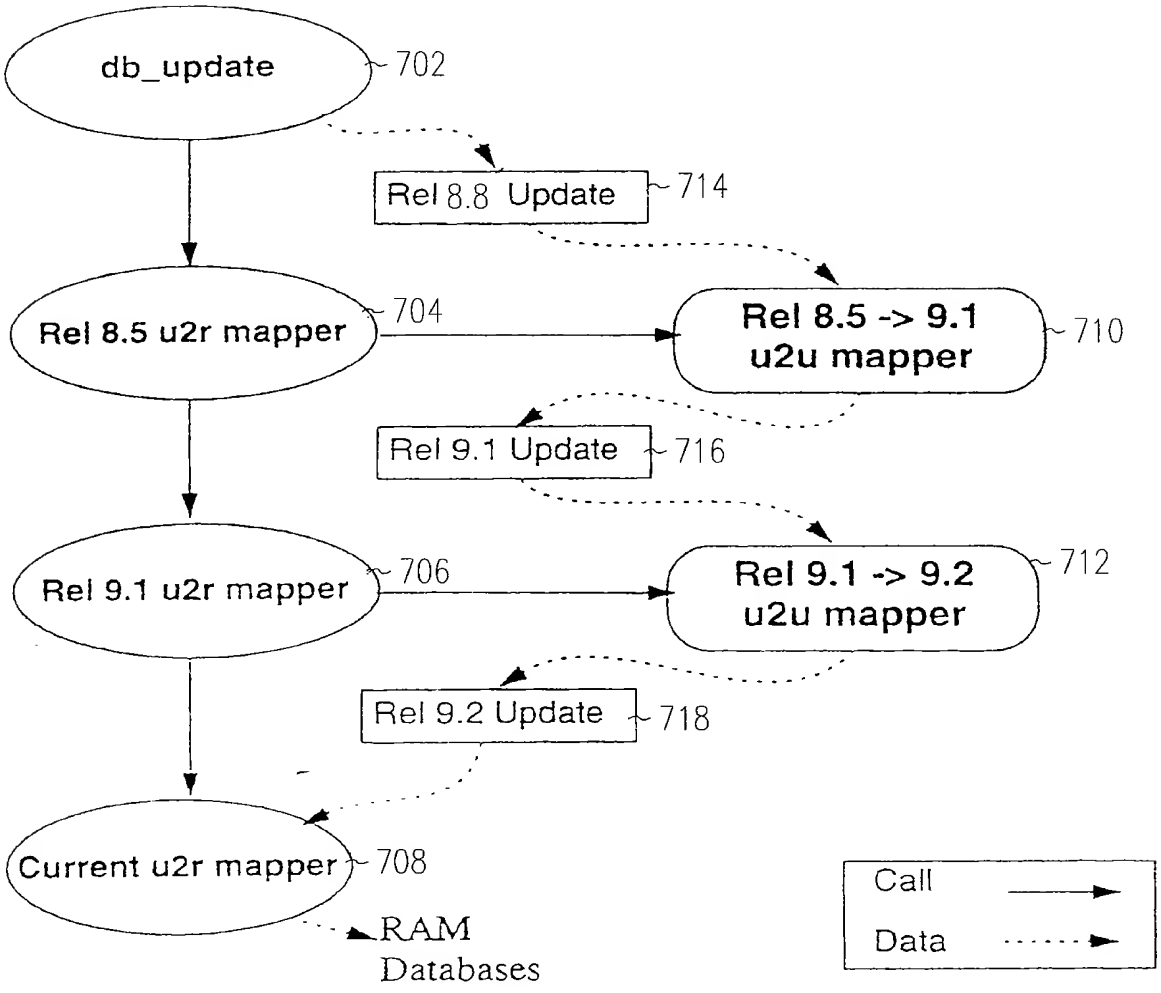
db_update
702
Rel 8.8 Update
714
Rel 8.5 u2r mapper
704
Rel 8.5 -> 9.1 u2u mapper
710
Rel 9.1 Update
716
Rel 9.1 u2r mapper
706
Rel 9.1 -> 9.2 u2u mapper
712
Rel 9.2 Update
718
Current u2r mapper
708
RAM Databases
Call
Data

Fig. 4

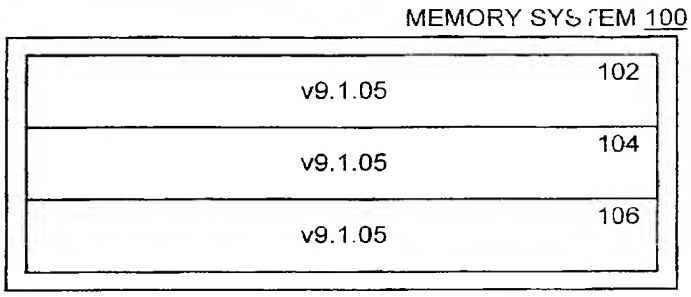

Fig. 5a

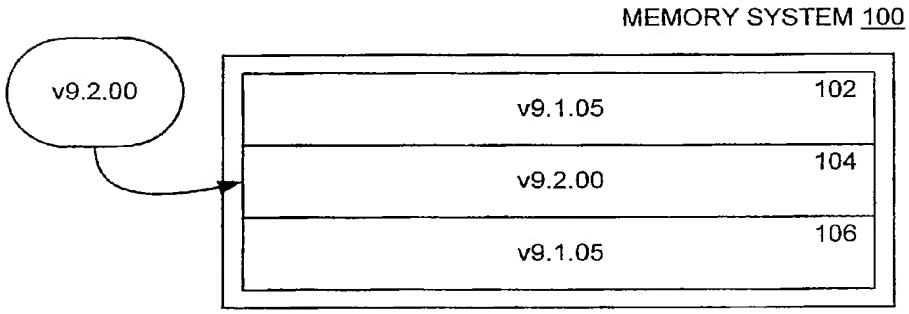

Fig. 5b

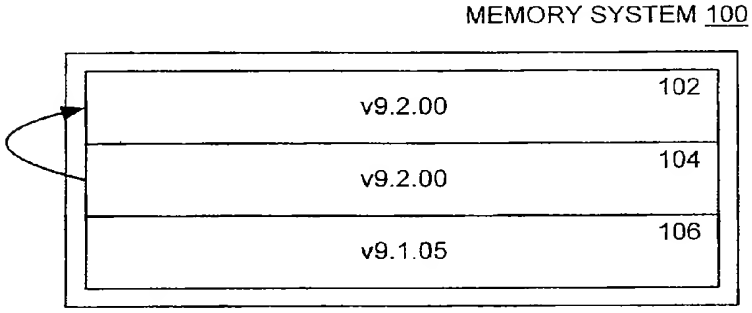

Fig. 5c

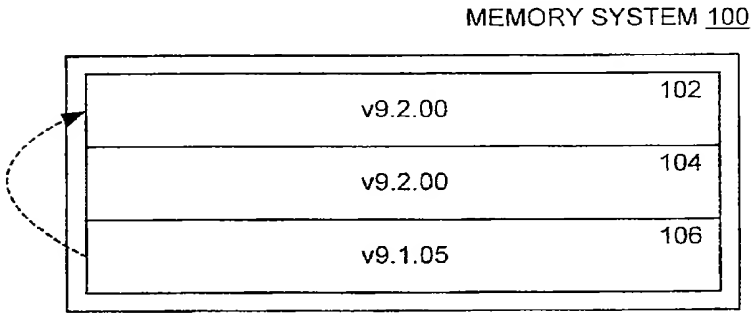

Fig. 5d

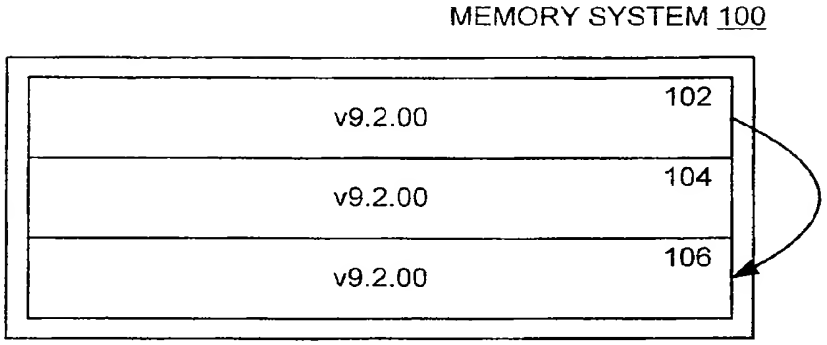

Fig. 5e

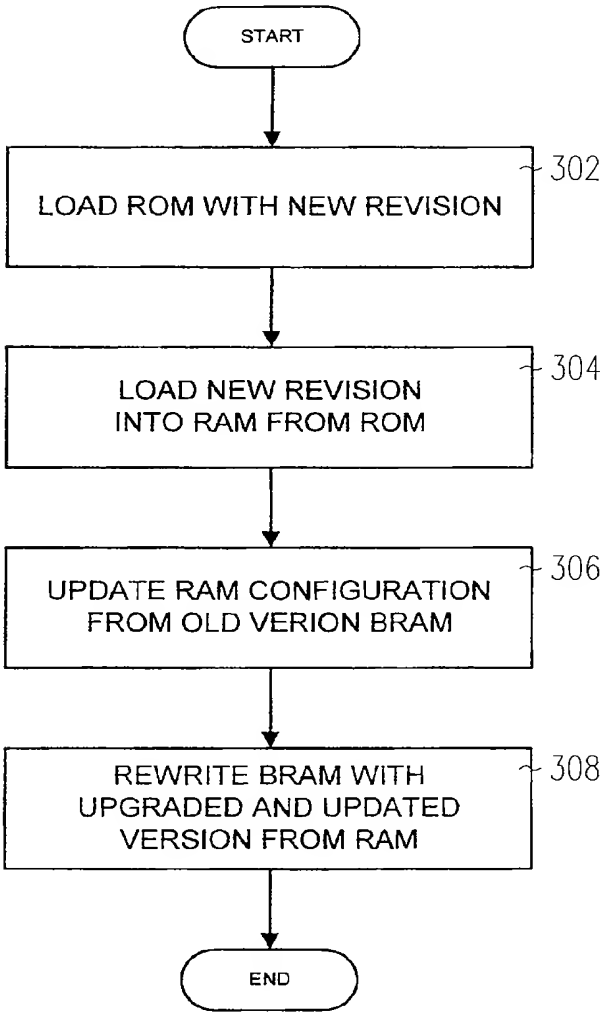
START
LOAD ROM WITH NEW REVISION
302
LOAD NEW REVISION INTO RAM FROM ROM
304
UPDATE RAM CONFIGURATION FROM OLD VERION BRAM
306
REWRITE BRAM WITH UPGRADED AND UPDATED VERSION FROM RAM
308
END

Fig. 6

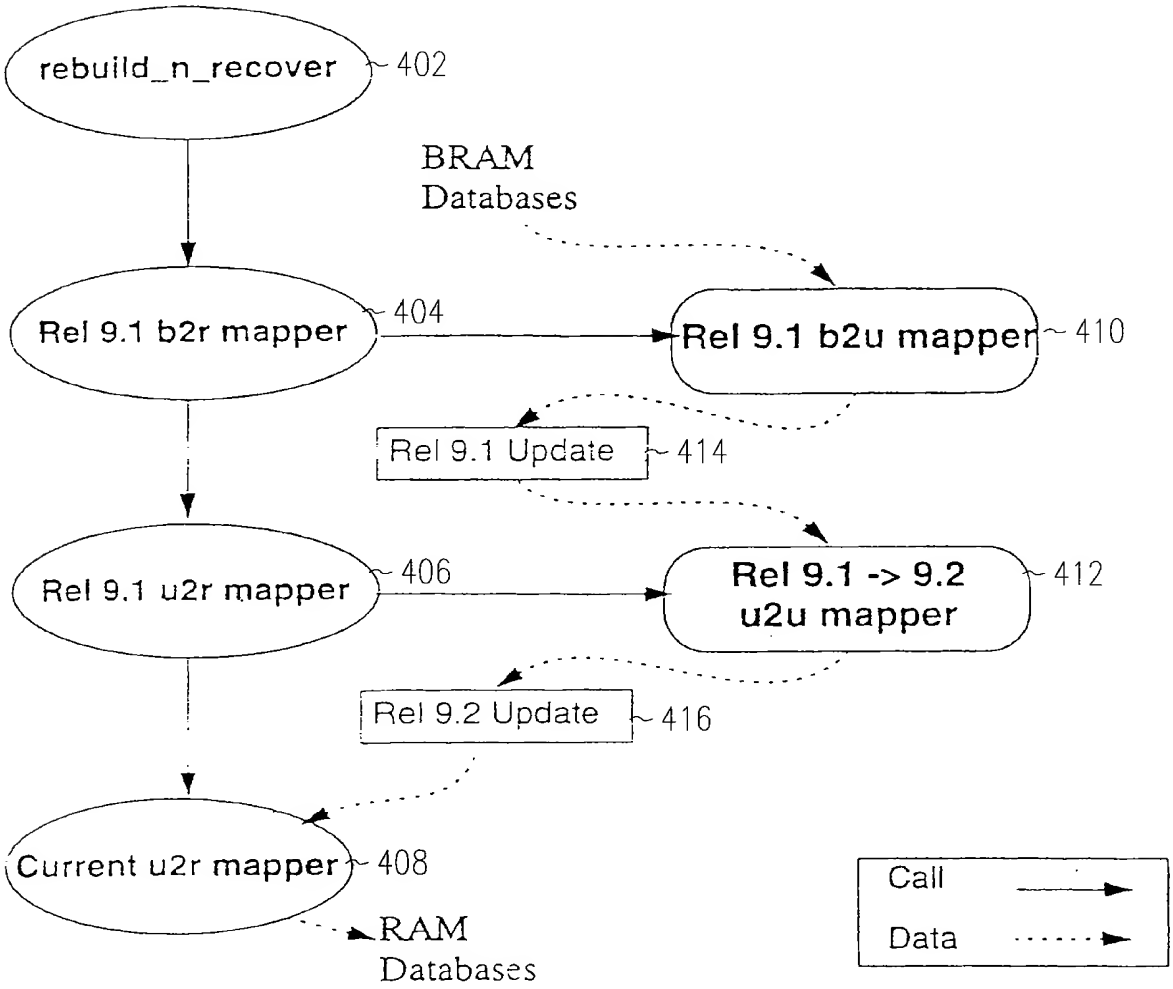
rebuild_n_recover
402
BRAM
Databases
Rel 9.1 b2r mapper
404
Rel 9.1 b2u mapper
410
Rel 9.1 Update
414
Rel 9.1 u2r mapper
406
Rel 9.1 -> 9.2
u2u mapper
412
Rel 9.2 Update
416
Current u2r mapper
408
RAM
Databases
Call
Data

Fig. 7